

**Date:19.9.2025**

**Notification for Recruitment for Senior Research Fellow**  
**Project funded by MeITY Chips to Startup Program, Ref: IIITA/C2S/2023/01**

- **Project Areas:** CMOS Transceiver Chip Design, fabrication and Test for Indian-Long Range Frequency band.
- **Required Skills:**
  - a. Strong Analog/RF Integrated Circuit Design skills using Cadence EDA tools.
  - b. Good knowledge of MOSFETs.
- **No. of Positions:** One
  - a. Senior Research Fellow (One)
- **Salary Particulars:** The monthly remuneration for this position will be as follows.
  - a. Senior Research Fellow: 42000 Rs (Fourty Two Thousand Rupees Only).
- **Eligibility for Senior Research Fellow:** B.E./B.Tech. and M.E./ M.Tech from reputed institutions with specialization in areas allied to the advertised project areas and with minimum of 60% marks and having GATE qualified once during any year in the academic career.
- **Appointment Duration:** Initially for one year with the possibility of an extension depending on performance and availability of funding.

Hostel accommodation may be provided at the institute based on availability on a chargeable basis. In case of non-availability of institute hostel, admissible HRA will be provided as per Government of India rules.
- **Deadline for application:** 16 Oct 2025
- **Schedule of Interview:** 27 Oct 2025, 10:00 AM, CC-1, 2106, IIIT Allahabad

Candidates satisfying the required skills may send their resumes and certificates pertaining to educational qualifications starting from class X (in a single PDF) to “[prasanna@iiita.ac.in](mailto:prasanna@iiita.ac.in)”. The subject of the e-mail application should be **Application for Senior Research Fellow-IIITA/C2S/2023/01**. The candidate is expected to join immediately after receiving the offer letter.