

Date:18.8.2025

**Notification for Recruitment for Postdoctoral Fellow
Visvesvaraya PhD Scheme for Electronics and IT: Phase II at IIIT Allahabad**

- **Project Areas:** CMOS Transceiver Chip Design, fabrication and Test.
- **Required Skills:**
 - a. Strong Analog/RF Integrated Circuit Design skills using Cadence EDA tools.
 - b. Good knowledge of MOSFETs.
- **No. of Positions:** One
 - a. Postdoctoral Fellow (One)
- **Salary Particulars:** The monthly remuneration for this position will be as follows.
 - a. **Postdoctoral Fellow:** Rs 1,08,393/- per month (One Lakh eight Thousand three hundred ninety three Only).
- **Eligibility for Postdoctoral Fellow:** PhD from reputed institutions with specialization in areas allied to the advertised project areas.
- **Appointment Duration:** one year
Hostel accommodation may be provided at the institute based on availability on a chargeable basis. In case of non-availability of institute hostel, admissible HRA will be provided as per Government of India rules.
- **Deadline for application:** 3 Sept 2025
- **Schedule of Interview:** 9 Sept 2025, 10:00 AM, CC-1, 2106, IIIT Allahabad

Candidates satisfying the required skills may send their resumes and certificates pertaining to educational qualifications starting from class X (in a single PDF) to “prasanna@iiita.ac.in”. The subject of the e-mail application should be **Application for Postdoctoral Fellow-ESDM**. The candidate is expected to join immediately after receiving the offer letter.

Eligibility:

1. The PDF is awarded to researchers with a proven academic record, including peer-reviewed publications and recognitions in the highlighted research area.
2. It is a full-time, non-transferable, temporary position tenable in India only. The fellowship is valid for 1 year.
3. The PDF applicant should be an Indian citizen.
4. PDF applicants currently in regular employment will not be considered. However, scientists or researchers with temporary positions in academia or research institutions would be considered, but they would be required to relinquish their current roles if selected for the fellowship.
5. The PDF applicant must have obtained a PhD degree from a recognized University with first class (in terms of grades, etc.) in all preceding levels and a good academic record throughout.
6. The PDF applicant should have completed PhD within the past 5 years on the last date of submission of application to the institution.
7. PDF applicants should not have completed their PhD at the same institution (host institution) where the PDF fellowship is to be awarded.
8. The upper age limit for the fellowship is 40 years on the last date of application.