Following are the candidate shortlisted for selection Test/Interview for the Ph.D. Programme in the Department of Electronics and Communications Engineering at IIIT-Allahabad. The Test/Interview will be held on **January 4, 2016 from 9:00AM** onwards. The candidates are requested to come prepared for a test/Interview.

There may be two separate panels as per the areas of specialization: (a) Communication/Signal Processing/RF systems. (b) Microelectronics/VLSI/Embedded Systems.

Candidates are requested to bring all documents in Original including the GATE Score Card which will be checked before the interview.

Sr. No.	Registration ID	Full Name
1		Mr. Manog Tolani
2		Mr. Surender Hans
3		Yogesh Kumar Verma
4	IIITA-RES-003	FAIZAN AHMAD
5	IIITA-RES-004	PATRI UPENDER
6	IIITA-RES-019	Aman Kumar
7	IIITA-RES-024	Vivek Rajpoot
8	IIITA-RES-027	Saurabh Bhaskar
9	IIITA-RES-028	AJAY KUMAR TIWARI
10	IIITA-RES-030	shobhit kumar srivastava
11	IIITA-RES-035	GHANSHYAM SINGH
20	IIITA-RES-038	HIMANI MISHRA
12	IIITA-RES-049	ABHISHEK KUMAR
12	IIITA-RES-128	ABHISHEK KUMAR
13	IIITA-RES-053	shailendra chauhan
14	IIITA-RES-066	MRIDULA MAURYA
15	IIITA-RES-088	Nidhi Verma
16	IIITA-RES-118	Sachin Taran
17	IIITA-RES-228	ravish kumar singh
18	IIITA-RES-231	SHASHI BHUSHAN SHARMA
19		Soumit Samdder Choudhury