

Department of Applied Sciences Indian Institute of Information Technology Allahabad Allahabad-211012 (UP) India

Dated 17/03/2023

Advertisement for the Post of Project Associate-I

Applications are invited for the post of Project Associate-I (on contract) in a research project titled "Nanometer scaled Topological Insulators for future micro and opto electronic applications" in reference to the sanction letter no. of File No. CRG/2022/000070 dated: 04.01.2023 by Science and Engineering Research Board (SERB), Department of Science & Technology, New Delhi-110070. The duly completed application in the prescribed format along with copies of supporting documents must reach to Dr. Pramod Kumar, Department of Applied Sciences, Room No. LT-3108, Indian Institute of Information Technology Allahabad, Deoghat, Jhalwa, Allahabad, Uttar Pradesh 211012 on or before 20/04/2023 with the subject as "Application for the post of Project Associate-I in the SERB Research Project- File No. CRG/2022/000070". A soft copy of the application should also be sent to pkumar@iiita.ac.in (PI of the project).

Title of the Project: Nanometer scaled Topological Insulators for future micro and opto electronic applications **Name of Position:** Project Associate-I

Number of Positions: 01 (one)

Project Duration: Initially up to one year, this may be extended for further period(s) up to 3 years based on the satisfactory performance of individual.

Age Limit: Maximum 30 Years

Eligibility Criterion:

- (i) The candidate MUST have secured at least 65% cumulatively or equivalent CGPI at the time of application submission itself. Candidates in all cases shall be required to be clear PASS with 65% Marks or Equivalent Cumulative Grade Point Index before finally joining the program. A 5% relaxation shall be applicable for SC/ST/PHCandidates.
- (ii) Candidates with M.Sc. (Electronics/Solid State Physics)/M.Tech. (Solid State Physics/Electronics/Nanotechnology/Material Sciences) Backgrounds
- (iii) For registration in PhD valid GATE or UGC/CSIR NET will required.
- (iv) Furthermore, the applicant will be preferred if s/he has sound knowledge of Topological Insulator,
 Semiconductor Physics, Device simulation / practical knowledge of basic thin film fabrication steps/
 thin film surface characterizations, Hall effect, XRD, Ultrafast Dynamics, Spintronics etc,.

(v) Candidate should have good communication skill in verbal and written English.

Fellowship and HRA: 1. As per SERB order No.SB/S9/Z-07/2020.

Note(s)

- 1. The candidate should be able to travel across various Institutes/Research Centre's for the project related work whenever needed.
- 2. The applicant will be responsible for the authenticity of the information, other documents, and photographs submitted.
- The institute reserves the right to accept/reject the application at any time, and/or consider candidates of
 exceptional credentials without applications. Qualification and experience may be relaxed at any point of
 time for exceptional candidates.
- 4. Mere, possessing the prescribed qualification does not ensure that the candidate will be called for an Interview. The candidates will be shortlisted on the basis of merit and need of the project.
- 5. Shortlisted candidates will be informed by e-mail about the interview date. So, the candidate must provide valid e-mail ID(s) in their application.
- 6. Shortlisted candidates have to present themselves for the interview on the interview date with updated CV and original and attested photocopies of mark sheets/certificates in support of their academic qualifications.
- 7. No TA/DA will be paid for appearing in the interview.
- 8. The first and foremost responsibility of the candidate is to fulfill the jobs' requirements to complete the above-mentioned project in timely manner.
- 9. The association between PI and the candidate can be discontinued at any time by giving one month's notice in writing either by the PI of the project to the candidate, or by candidate to the PI of the project.
- 10. Candidate selected for this position may also apply for the regular Ph.D. program in AS Department, provided he/she satisfies the eligibility criterion followed by the selection process laid down by the institute (IIITA).
- 11. Hostel accommodation may be provided as per institute norms

Dr. Pramod Kumar Principal Investigator (PI) Department Applied Sciences IIIT-Allahabad-211012 (U.P.)-India



Department of Applied Sciences Indian Institute of Information Technology Allahabad Allahabad-211012 (UP) India

APPLICATION FORM

Dated 17/03/2023

Research Project Entitled- "Nanometer scaled Topological Insulators for future micro and opto

electronic applications"

File No.:		CRG/2022/0	00070						
Funding Agency:		Science and Engineering Research Board (SERB), New Delhi							
Name of P.I.:		Dr. Pramod Kumar							
Na	me of position:	Project Asso	ciate-I						
Department/Section:		Department of Applied Sciences							
	Name of the Candidate (In Capital):								
2.	. Sex (Male/Female):								
3.	Marital Status (Married/Unmarried):								
4.	. Date of Birth:								
5.	Age (as on last date of application): Years Months Days								
6.	Father's/Husband's Name:								
7.	Present/Contact Address:								
8.	Permanent Address:								
9.	Mobile No:								
10. E-mail:									
11.	Nationality:								
12.	12. Category Gen/EWS/SC/ST/OBC/PH:								
	GATE/NET:								
B	ranch		Year	Valid up to	Percentile (& Scor	e)	All India Rank		

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15. Educational Qualification (10thonward):

Qualification	Subject/Discipline	Board/Institute/Univ.	Year of passing	% of marks	Division
10th					
12th					
B.Sc.					
B.E./B.Tech/M.Sc.					
M.E./M.Tech					

16. Summary of M.E/M.Sc./M.Tech Thesis (Maximum 150 words): Enclose a separate sheet

- 17. Research Publications (Maximum Best 5): Enclose a separate sheet
- 18. Details of Experience in chronological order. Enclose a separate sheet
- 19. Any other information: Enclose a separate sheet
- 20. Indicate whether you propose to register for higher degree at IIITA, Allahabad: Yes/No (Name of higher degree is Ph.D.)

DECLARATION

I hereby declare that all the statements made in this application are true and complete, and nothing has been concealed/distorted. I am aware that, if at any time I am found to have concealed/distorted any material information, my engagement is liable to be summarily terminated without notice.

Place: Date:

Checklist-

- 1. Self-attested copy of documents
- 2. Proof of Date of Birth
- 3. Mark sheets & certificates of all the examinations passed
- 4. Category Certificate, if applicable
- 5. GATE/NET score card, if passed
- 6. Experience certificate, if any

Signature of the Applicant