**Department of Electronics and Communication Engineering**

**Indian Institute of Information Technology Allahabad**

**Prayagraj 211015 (UP) India**

**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_**

**Advt. No. IIITA/ECE/PHY/01/2019 Dated – 07/01/2019**

**Advertisement for the Post of Junior Research Fellow (JRF) (On Contract)**

Applications are invited for the post of Junior Research Fellow (JRF) (on contract) in research project entitled **“Physical Layer Security in Cooperative Cognitive Relay Networks”** in reference to the sanction letter no. File No. ECR/2017/000104 dated: 03.06.2017 by Science and Engineering Research Board (SERB), Department of Science & Technology, NewDelhi-110070. The duly completed application on prescribed format along with copies of supporting documents must reach to **Dr. Suneel Yadav**, **Department of Electronics & Communication Engineering, Room No. 2106, Computer Centre – 1, Indian Institute of Information Technology Allahabad, Devghat, Jhalwa, Allahabad, Uttar Pradesh 211012 on or before 31/01/2019.** The envelope should be super scribed as **“Application for the post of Junior Research Fellow (JRF) (on contract) in the Research Project “Physical Layer Security in Cooperative Cognitive Relay Networks”**. A soft copy of the application should also be sent to suneel@iiita.ac.in **(PI of the project).**

**Title of the Project:** Physical Layer Security in Cooperative Cognitive Relay Networks

**Name of Position:** Junior Research Fellow (JRF)  
**Number of Position:** 01 (one)  
**Project Duration:** One and Half (1.5) Years, Completion Date: 09/06/2020

**Age Limit:** Maximum 35 Years as on 31/01/2019.

**Essential Qualifications:** M.E/M.Tech in Electronics/Electrical with specialization in Wireless Communication or equivalent.

**Desirable Qualifications:** (i) A GATE/NET or similar other national level ranking.

(ii) Knowledge of Random Variable Theory and Convex Optimization.

(iii) Exposure of MATLAB programming and **SOFTWARE DEFINED RADIO (SDR)**.

(iv) The preference may be given to the candidates who published papers in repute Journals/Conferences.

**Fellowship:** 25,000/- pm (Fixed).

**Note(s)**

1. The applicant will be responsible for the authenticity of information, other documents and photographs submitted.
2. The Institute reserves the right to accept application at any time, and consider candidates of exceptional credentials without applications. Qualification and experience may be relaxed at any point of time for exceptional candidates.
3. Mere, possessing the prescribed qualification does not ensure that the candidate would be called for Interview. The Candidates will be shortlisted on the basis of merit and need of the project.
4. Shortlisted Candidates will be informed by e-mail about the interview date. So, the candidate must provide valid E-mail IDs in their applications.
5. Shortlisted candidates have to present themselves for the interview on the interview date with updated CV and original and attested photocopies of mark sheets/certificates in support of their academic qualifications.
6. Applicants in employment (private, government or any other organization) are required to submit a “**No Objection Certificate**” from the employer at the time of interview.
7. No TA/DA will be paid for appearing in the interview.
8. The first and foremost responsibility of the candidate is to fulfil the jobs’ requirements to complete the above mentioned project in timely manner.
9. The association between PI and the candidate can be discontinued at any time by giving one month’s notice in writing either by the PI of the project to candidate, or by candidate to the PI of the project.
10. Candidate selected for this position can also apply for the regular PhD program in ECE Department, provided they satisfy the eligibility criterion followed by selection process laid down by the institute.
11. After qualifying the PhD admission / selection test the candidate will be registered as regular PhD scholar and eligible to draw the fellowship and other allowances as per the Institute rules.

**Dr. Suneel Yadav  
Principal Investigator (PI)**

**Department of Electronics and Communication Engineering**

**Indian Institute of Information Technology Allahabad-211012 (U.P.)-India**

**Department of Electronics and Communication Engineering**

**Indian Institute of Information Technology Allahabad**

**Prayagraj 211015 (UP), India**

**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_**

**Advt. No. IIITA/ECE/PHY/01/2019**

**For office use only**

Application No…………….

**APPLICATION FORM**

**Advt. No……………………… Dated…………………**

**Research Project Entitled- “Physical Layer Security in Cooperative Cognitive Relay Networks**”

**File No. :** ECR/2017/000104

**Funding Agency:** Science and Engineering Research Board (SERB), New Delhi

**Name of P.I.:** Dr. Suneel Yadav

**Name of position**: Junior Research Fellow (on contract)

**Department/Section:** Department of Electronics and Communication Engineering

1. Name of the Candidate (In Capital):
2. Sex (Male/Female):
3. Marital Status (Married/Unmarried):
4. Date of Birth:
5. Age (as on last date of application): \_\_\_\_\_\_\_\_\_\_Years \_\_\_\_\_\_\_\_\_Months \_\_\_\_\_\_\_\_\_Days
6. Father’s/Husband’s Name:
7. Present/Contact Address:
8. Permanent Address:
9. Mobile No:
10. E-mail:
11. Nationality:
12. Category Gen/SC/ST/OBC/PH:
13. GATE/NET

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Branch | Year | Valid up to | Percentile (& Score) | All India Rank |
|  |  |  |  |  |

1. Educational Qualification (10th onward):

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Qualification** | **Subject/Discipline** | **Board/Institute/Univ.** | **Year of passing** | **% of marks** | **Division** |
| 10th |  |  |  |  |  |
| 12th |  |  |  |  |  |
| Diploma, if any |  |  |  |  |  |
| B.E./B.Tech |  |  |  |  |  |
| M.E./M.Tech |  |  |  |  |  |

1. Summary of M.E/M.Tech Thesis (Maximum 150 words): ***Enclose a separate sheet***
2. Research Publications (Maximum Best 5): ***Enclose a separate sheet***
3. Details of Experience in chronological order. ***Enclose a separate sheet***
4. Any other information: ***Enclose a separate sheet***
5. Indicate whether you propose to register for higher degree at IIITA, Allahabad: Yes/No

(Name of higher degree is Ph.D.)

**DECLARATION**

I hereby declare that all the statements made in this application are true and complete and nothing has been

concealed/ distorted. I am aware that, if at any time I am found to have concealed/distorted any material information, my engagement is liable to be summarily terminated without notice.

Place: Signature of the Applicant

Date:

**Checklist-**

1. Self-attested copy of documents
2. Proof of Date of Birth
3. Mark sheets & certificates of all the examination passed
4. Category Certificate, if applicable
5. GATE/NET score card, if passed
6. Experience certificate, if any